



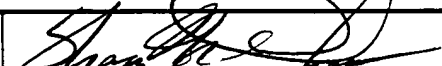
Substitute for Form 1449/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>					<b>Complete if Known</b>	
					Application Number	10/798,600
					Filing Date	March 10, 2004
					First Named Inventor:	Rohit Natarajan
					Art Unit	2186
					Examiner Name	Thomas, Shane M.
					Attorney Docket Number	42P17960
Sheet	1	of		2		
<b>U.S. PATENT DOCUMENTS</b>						
Examiner Initials*	Cite No. <sup>1</sup>	Patent Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup>	Number <sup>4</sup> Kind Code <sup>5</sup> (if known)			
	1	US-	5,903,916	05-11-1999	Pawlowski et al.	
	2	US-	6,172,893 B1	01-09-2001	Ryan	
	3	US-	6,269,433 B1	07-31-2001	Jones et al.	
	4	US-	6,427,196 B1	07-30-2002	Adiletta et al.	
	5	US-	6,532,509 B1	03-11-2003	Wolrich et al.	
	6	US-	6,560,667 B1	05-06-2003	Wolrich et al.	
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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	7	WO	02/33556	A2	04-25-2002	SUN MICROSYSTEMS, INC.		

Examiner Signature		Date Considered	3/19/07
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application: Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

<b>Substitute for Form 1449/PTO</b> <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
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				Art Unit	2186
				Examiner Name	Thomas, Shane M.
Sheet	2	of	2	Attorney Docket Number	42P17960
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
<i>bm</i>	8	INTEL, Intel Technology Journal, The next Generation of Intel IXP Network Processors, Volume 06, Issue 03, 8/15/2002, pp. 1-15.			
	9	MICRON, RLD RAM II Memory, Low-Latency, High-Density, High-Bandwidth DRAM, 12/12/2003, (2 pgs.).			
	10	INTEL, Intel IXP1200 Network Processor Family, Hardware Reference Manual, December 2001, pp. 1-272.			
	11	MICRON Technical Note: TN-49-02 Exploring the RLD RAM II Feature Set; 2004, pp. 1-4.			
	12	MICRON Technical Note: TN-49-05 General DDR SDRAM Functionality; 2001, pp. 1-11.			

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